



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Shieh, *et al.* Docket No.: TSM02-1241
Serial No.: 10/721,979 Art Unit: 2812
Filed: November 25, 2003 Examiner: TBD
For: Method of Fabricating Semiconductor Device with Separate Periphery and Cell Region Etching Steps

Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

Date of Deposit: March 3, 2004

I hereby certify that the below listed correspondence is being deposited with the United States Postal Service on the date indicated above as first class mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

Certificate of Mailing via First Class Mail (1 page)
Information Disclosure Statement (1 page)
PTO/SB/08a with 4 References Cited (1 page)
Copy of 2 Cited References
Return Postcard

Respectfully submitted,


Kristin Hayes
Legal Assistant

Slater & Matsil, L.L.P.
17950 Preston Rd., Suite 1000
Dallas, TX 75252
Tel: 972-732-1001
Fax: 972-732-9218



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Shieh, *et al.* Attorney Docket: TSM02-1241
Filed: November 25, 2003 Examiner: TBD
Serial No.: 10/721,979 Art Unit: 2812
For: Method of Fabricating Semiconductor Device with Separate Periphery and Cell Region Etching Steps

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

The Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(3), before the mailing of a first Office action on the merits.

Cite numbers A-B are newly cited prior art being filed under the waived requirement of 37 CFR 1.98 (a)(2)(i) dated July 11, 2003, therefore, copies of these U.S. patents are not included.

Respectfully submitted,

3-2-04
Date

Slater & Matsil, L.L.P.
17950 Preston Rd., Suite 1000
Dallas, TX 75252
(972) 732-1001 (phone)
(972) 732-9218 (fax)


Roger C. Knapp
Attorney for Applicant
Reg. No. 46,836



Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Substitute for form 1449A/PTO

Complete if Known

Application Number	10/721,979
Filing Date	11/25/2003
First Named Inventor	Shieh, et al.
Art Unit	2812
Examiner Name	TBD

Sheet 1 of 1 Attorney Docket Number TSM02-1241

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (<i>if known</i>)			
A		US-6,448,608 B1	09-10-2002	Pham, et al.	
B		US-6,559,501 B2	05-06-2003	Sung, et al.	
US-					

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (<i>if known</i>)				

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	C	OEHRLEIN, G.S., et al., "Surface Science Issues in Plasma Etching," [on-line] IBM Journal of Research and Development, Vol. 43, 1/2 (1999), Retrieved From: http://www.research.ibm.com/journal/rd/431/oehrlein.html	
	D	"Technical Comparison of Floating Gate Reprogrammable Nonvolatile Memories," Technical Paper, Silicon Storage Technology, Inc. (November 2001) pp. 1-8.	

Examiner Signature	Date Considered
--------------------	-----------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.